



PTO/SB/08A (07-05)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1 of 3

Complete If Known

Application Number	10/632,769
Filing Date	07/31/2003
First Named Inventor	Steven Sholtis
Art Unit	2165
Examiner Name	Diane D. Mizrahi
Attorney Docket Number	CA7035172003

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
4	1	US- 5,862,223	01/19/1999	Walker et al.	
	2	US- 5,878,408	03/02/1999	Van Huben et al.	
	3	US- 5,933,356	08/03/1999	Rostoker et al.	
	4	US- 6,058,426	05/02/2000	Godwin et al.	
	5	US- 6,102,961	08/15/2000	Lee et al.	
	6	US- 6,141,724	10/31/2000	Butler et al.	
	7	US- 6,269,467 B1	07/31/2001	Chang et al.	
	8	US- 6,594,799 B1	07/15/2003	Robertson et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
4	9	EP 0 433 066 A2	06-19-91	Hewlett-Packard Company		
	10	EP 0 433 066 A3	05-26-93	Hewlett-Packard Company		
	11	EP 0 944 002 A1	09-22-99	Sony Europe GmbH		
	12	EP 1 063 599 A2	12-27-00	Fujitsu Limited		
	13	EP 1 063 599 A3	10-08-03	Fujitsu Limited		
	14	WO 01/42969 A2	06-14-01	Cadence Design Systems, Inc.		
	15	WO 01/42969 A3	06-14-01	Cadence Design Systems, Inc.		
	16	WO 01/65422 A2	09-07-01	Cadence Design Systems, Inc.		
	17	WO 01/65422 A3	09-07-01	Cadence Design Systems, Inc.		
	18	WO 01/65423 A2	09-07-01	Cadence Design Systems, Inc.		
	19	WO 01/65423 A3	09-07-01	Cadence Design Systems, Inc.		

Examiner
Signature

DIANE MIZRAHI

Date
Considered

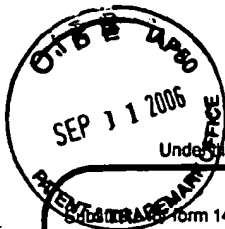
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Sheet 2 of 3

Application Number	10/632,769
Filing Date	July 31, 2003
First Named Inventor	Steven SHOLTIS
Art Unit	2165
Examiner Name	Diane D. Mizrahi
Attorney Docket Number	CA7035172003

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher city and/or country where published	T ²
le	1	Benini, L. et al., "Distributed EDA tool integration: the PPP paradigm", Proceedings of the International Conference on Computer Design, Oct. 1996, pp. 448-453, IEEE.	
	2	BLANCHARD, D. "EDA on the WWW PCB CAD", Printed Circuit Design (Feb. 1998) 15(2):34-37, Miller Freeman.	
	3	Brglez, F. et al., "A universal client for distributed networked design and computing", Jun. 2001, IEEE, pp. 401-406	
	4	CHAN, F.L. et al. "WELD--An Environment for Web-Based Electronic Design", Proceedings of the 35 th Annual Conference on Design Automation (May 1998), pp. 146-151	
	5	DALPASSO, M. et al. "Specification and Validation of Distributed IP-based designs with JavaCAD", Proceedings of the Conference on Design, Automation and Test in Europe (March 1999) pp. 684-688.	
	6	Dalpasso, M. et al., "Virtual simulation of distributed IP-based designs", Jun. 1999, IEEE, 50-55.	
	7	Dieckman, D. et al., "DISCOE: Distributed Design and Analysis to Preserve Intellectual Property", Sep. 1998, IEEE, pp. 57-60	
	8	Geppert, L., "The Rising Tide of Web-Based Tools and Services Augurs the Next Sea Change in Electronic Design IC Design on the World Wide Web", IEEE Spectrum, Jun. 1998, vol. 35, No. 6, pp. 45-50.	
	9	Konduri et al. "A Framework for Collaborative and Distributed Web-Based Design", Proceedings of the 36 th ACM/IEEE Conference on Design Automation (June 1999), pp. 898-903	
	10	Larson, N. et al., "Managing Design Processes: A Risk Assessment Approach", Nov. 1996, IEEE, pp. 749-759	
	11	Lee, Dong-Eun et al., "CADIC: Computer-aided design on internet with cryptosystem", Oct. 1998, IEEE, pp. 2670-2674	
	12	MORETTI, G. "Got IP?" Printed Circuit Design (Oct. 1998) 15(10):31-33	
	13	ORFALI, R. et al. "Essential Client/Server Survival Guide" 1996, Katherine Showalter, 2.sup.nd ed., pp. 228-233 and 476	
	14	PERRIN, B. "Web-Based Circuit Engineering", Circuit Cellar INK (Mar. 1999) No. 104, pp. 28-30, 33, 35, 37-38	
	15	RAO, D.M. et al. "Web-Based Network Analysis and Design", ACM Transactions on Modeling and Computer Simulation (Jan. 2000) 10(1):18-38	

Examiner's
SignatureDIANE MIZRAHI
PRIMARY EXAMINER

Date

Considered

9/14/06

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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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